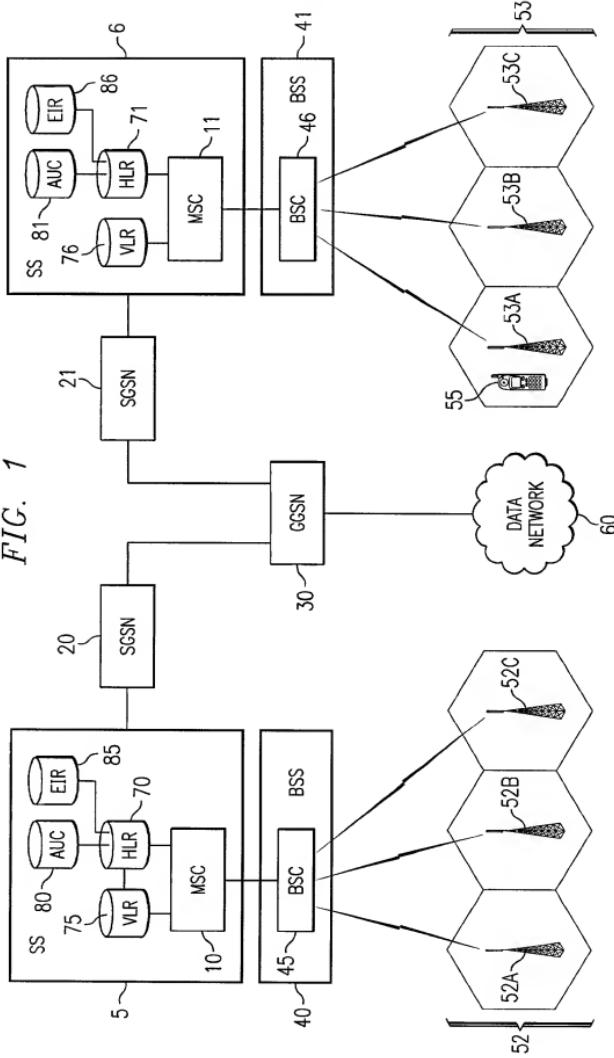


FIG. 1



2/2

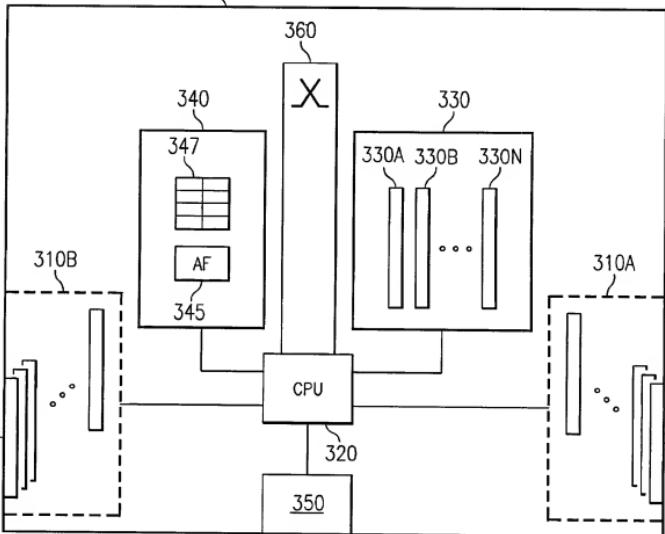


FIG. 2

Detailed memory map diagram. The memory is organized into two main sections: 348 (left) and 349 (right). Section 348 contains addresses 21, 23, 24, 25, 80, 1651, 1652, and 1653. Section 349 contains addresses 349A, 349B, 349C, 349D, 349K, 349L, 349M, and 349N. The memory is represented as a grid of cells, with 'XXXXXX--' indicating empty or unused memory. A pointer 347 points to the top of section 349.

FIG. 3